

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: David M. Fried, et al.

Examiner: Unassigned

Serial No.: To Be Assigned

Group Art Unit: Unassigned

Filed: Herewith

Docket: BUR920010120US2 (15040A)

For: Real Time Backup

Dated: February 9, 2004

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. U.S. Patent No. 3,798,753, dated March 26, 1974, issued to Camenzind, et al.;
2. U.S. Patent No. 4,065,742, dated December 27, 1977, issued to Kendall, et al.;
3. U.S. Patent No. 4,191,964, dated March 4, 1980, issued to Kant;
4. U.S. Patent No. 4,296,428, dated October 1981, issued to Tegzi P. Haraszti;
5. U.S. Patent No. 4,416,049, dated November 1983, issued to McElroy;
6. U.S. Patent No. 4,743,565, dated May 10, 1988, issued to Goth, et al.;
7. U.S. Patent No. 4,825,081, dated April 1989, issued to Wille, et al.;
8. U.S. Patent No. 4,893,166, dated January 9, 1990, issued to Geekie;
9. U.S. Patent No. 5,102,812, dated April 1992, issued to Caneau, et al.;

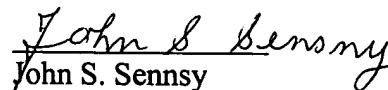
CERTIFICATE OF MAILING BY EXPRESS MAIL

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Date of Deposit: February 9, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Dated: February 9, 2004


John S. Sennsy

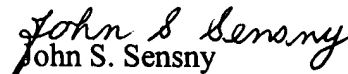
10. U.S. Patent No. 5,172,211, dated December 15, 1992, issued to Godinho, et al.;
11. U.S. Patent No. 5,287,377, dated February 1994, issued to Fukuzawa, et al.;
12. U.S. Patent No. 5,705,414, dated January 6, 1998, issued to Lustig;
13. U.S. Patent No. 5,916,821, dated June 29, 1999, issued to Kerber;
14. U.S. Patent No. 6,072,220, dated June 6, 2000, issued to Strack;
15. U.S. Patent No. 6,165,861, dated December 26, 2000, issued to Liu, et al.;
16. U.S. Patent No. 6,165,862, dated December 26, 2000, issued to Ishikawa, et al.;
17. U.S. Patent No. 6,297,533 B1, dated October 2001, issued to Aram Mikhitarian;
18. U.S. Patent No. 6,300,180 B1, dated October 2001 issued to Chang, et al.;
19. U.S. Patent No. 6,413,802 B1, dated July 2002, issued Hu, et al.;
20. U.S. Patent No. 6,545,297 B1, dated April 2003, issued to Noble, et al.; and
21. Yan, et al., "Scaling the Si MOSFET: From Bulk to SOI to Bulk", IEEE Transactions on Electron Devices, Vol. 39, No. 7, July 1992 (pp.1704-1710).

Pursuant to 37 C.F.R. §1.98(d), copies of the above listed references are not provided, as references were previously submitted to the Examiner and references were previously cited by the Examiner in connection with parent case, U.S. Serial Number: 09/683,625, filed on January 28, 2002.

Consideration of this Information Disclosure Statement is respectfully requested, since the art provided may be material to the examination of the present application as filed under 37 C.F.R. §1.56.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. §1.97(b), no statement or fee is required.

Respectfully submitted,


John S. Sensny
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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)				Atty. Docket N . BUR920010120US2 (15040A)		Serial N . Unassigned	
				Applicant David M. Fried, et al.			
				Filing Date Herewith		Group Unassigned	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL*		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
	AA	3,798,753	3/26/1974	Camenzind, et al.			
	AB	4,065,742	12/27/1977	Kendall, et al.			
	AC	4,191,964	3/4/1980	Kant			
	AD	4,296,428	10/1981	Tegzi P. Haraszti			
	AE	4,416,049	11/1983	McElroy			
	AF	4,743,565	5/10/1988	Goth, et al.			
	AG	4,825,081	4/1989	Wille, et al.			
	AH	4,893,166	1/9/1990	Geekie			
	AI	5,102,812	4/1992	Caneau, et al.			
Foreign Documents							
		Foreign Document Number	Date	Country	CLASS	SUBCLASS	TRANSLATION
							YES NO
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
		Yan, et al. "Scaling the Si MOSFET: From Bulk to SOI to Bulk", IEEE Transactions on Electron Devices, Vol. 39, No. 7, July 1992 (Pages 1704-1710).					
EXAMINER				DATE CONSIDERED			
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	AC	5,705,414	1/6/1998	Lustig			
	AD	5,916,821	6/29/1999	Kerber			
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	AA	6,413,802 B1	7/2002	Hu, et al.			
	AB	6,545,297 B1	4/2003	Noble, et al.			
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	AD						
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	AF						
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	AH						
	AI						

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